

Mark Earnshaw

A Senior System Architect at Coherent Logix since 2014. He has participated extensively in developing and revising the A/321 and A/322 ATSC 3.0 physical layer specifications while at Coherent Logix. Previously, Mark spent six years working on the LTE physical layer at BlackBerry, and eleven years in third- and fourth-generation cellular wireless at Nortel Networks. Dr Earnshaw is a co-inventor on 95 US patents and lives in Ottawa, Canada.

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Senior System Architect  
Coherent Logix